**CIRCUIT DIAGRAM (CLAMPER)**

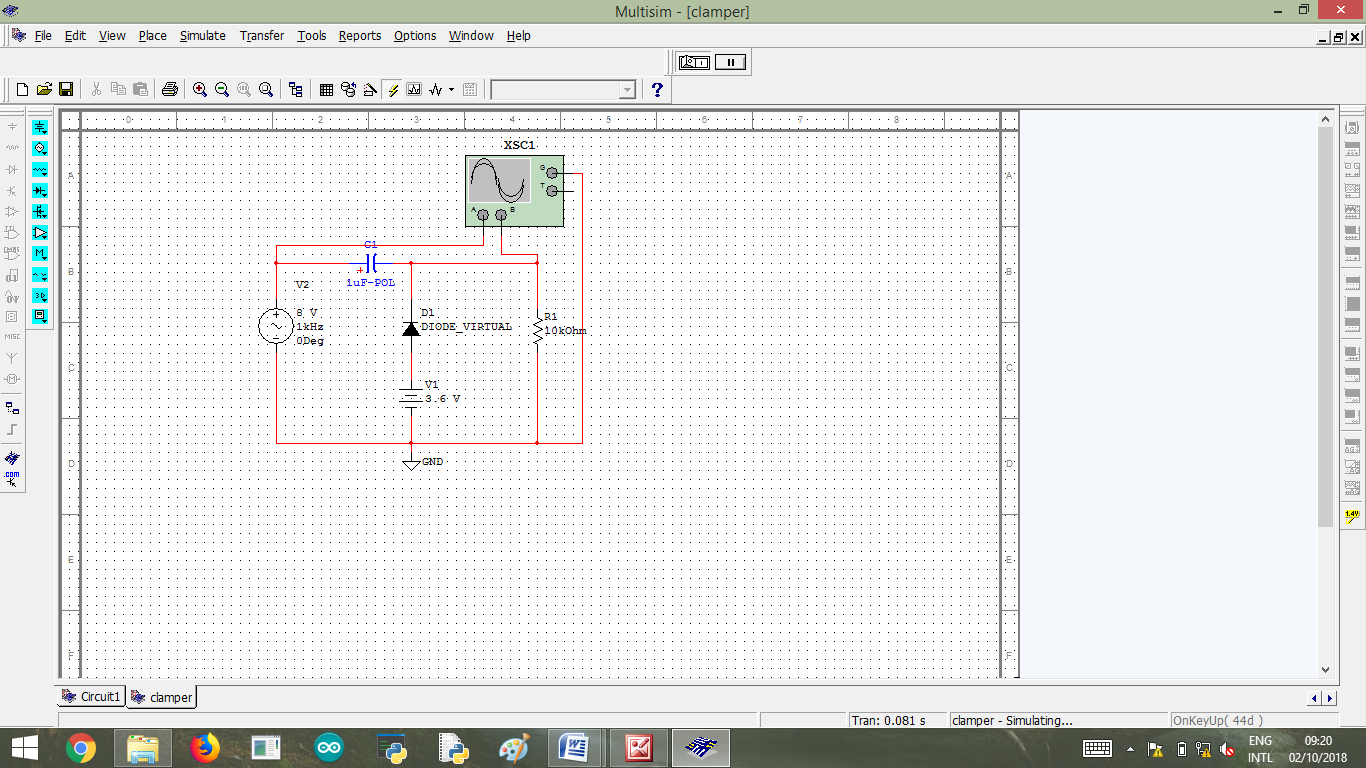


Fig 1: Clamper using multisim

**NET LIST**

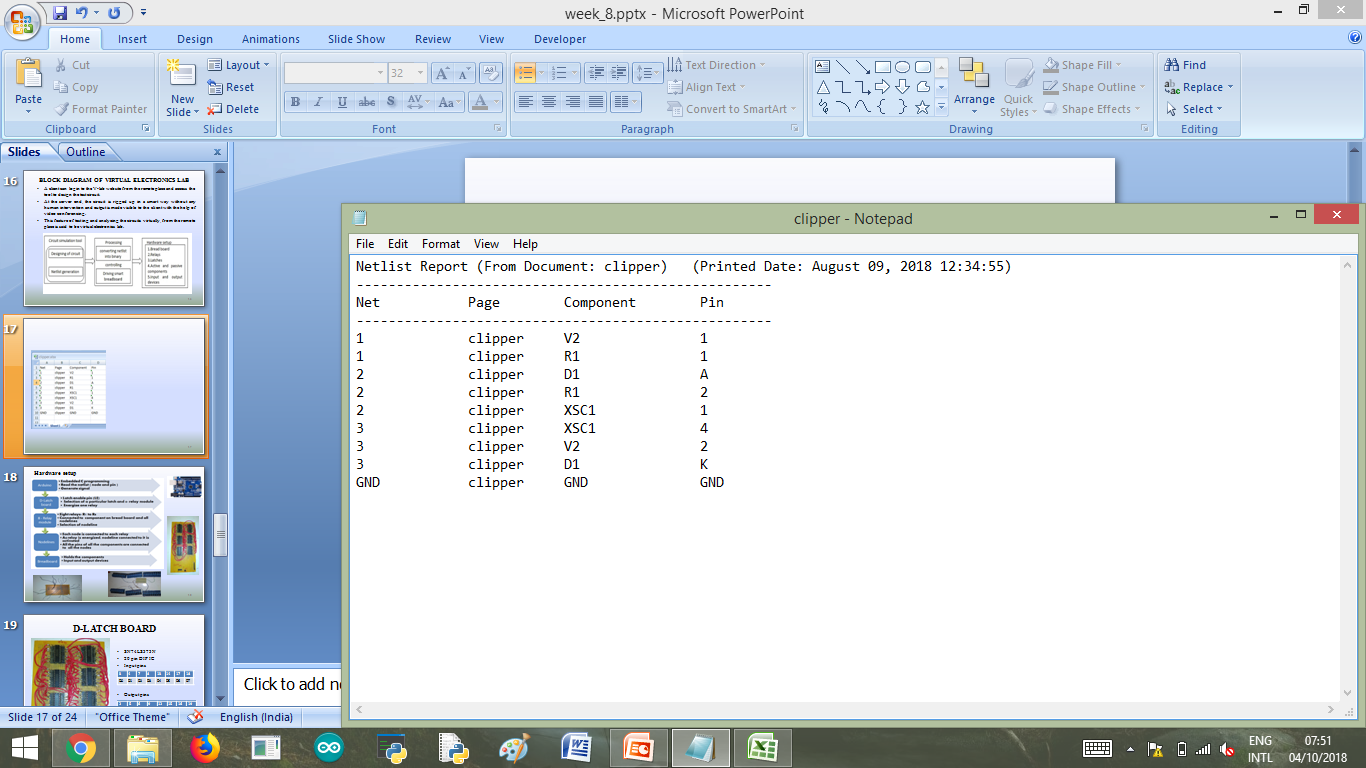
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Fig 2: Net list(Clamper)

**RELAY INTERFACING**

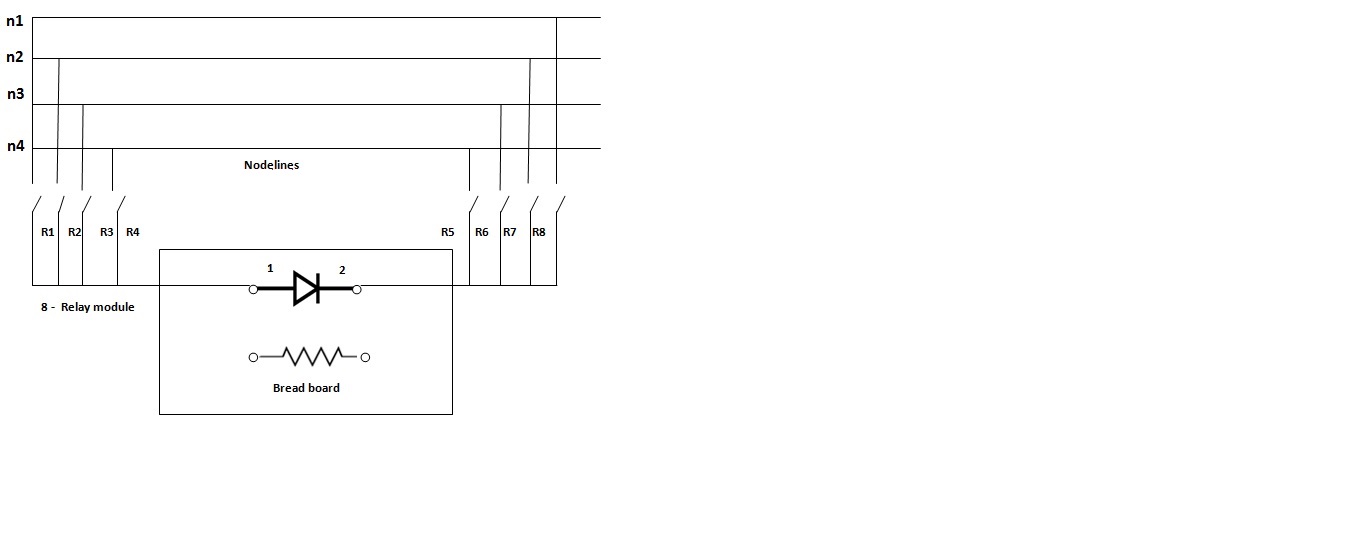
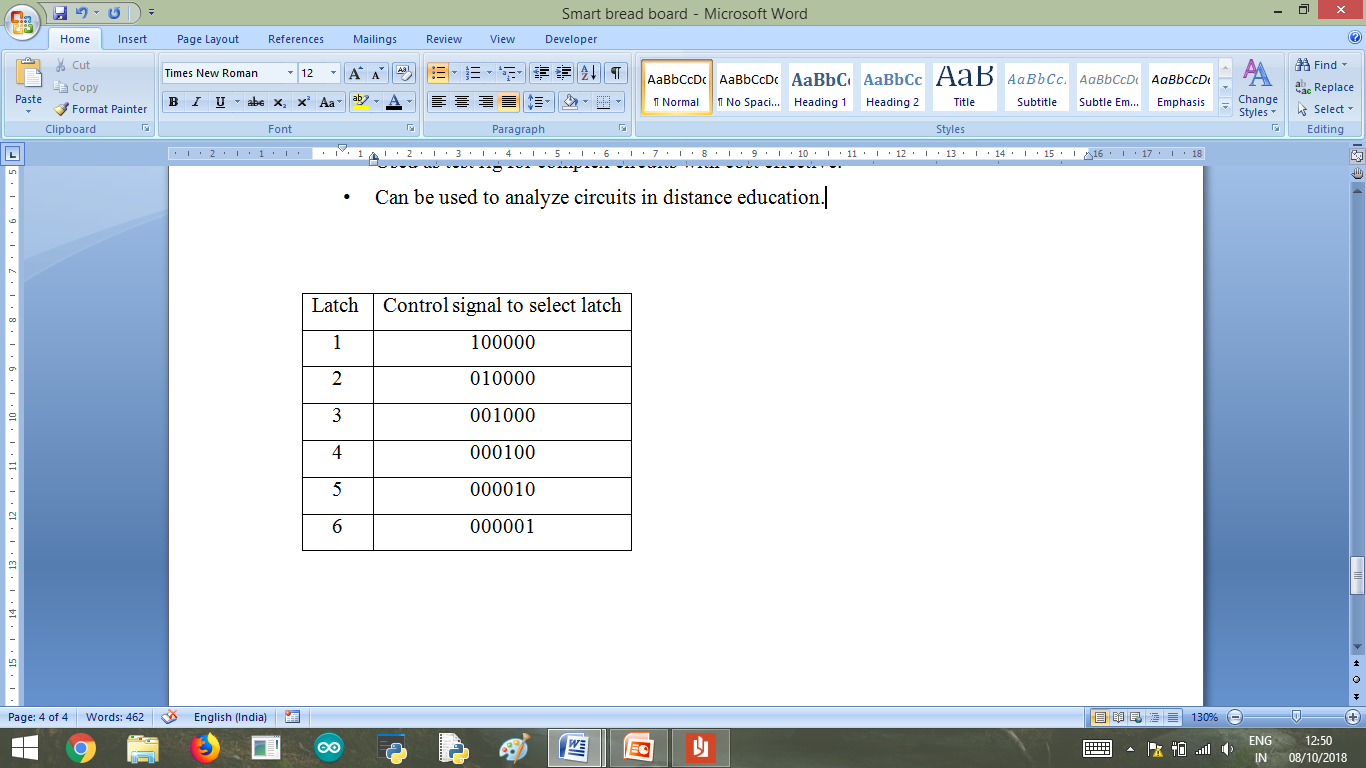
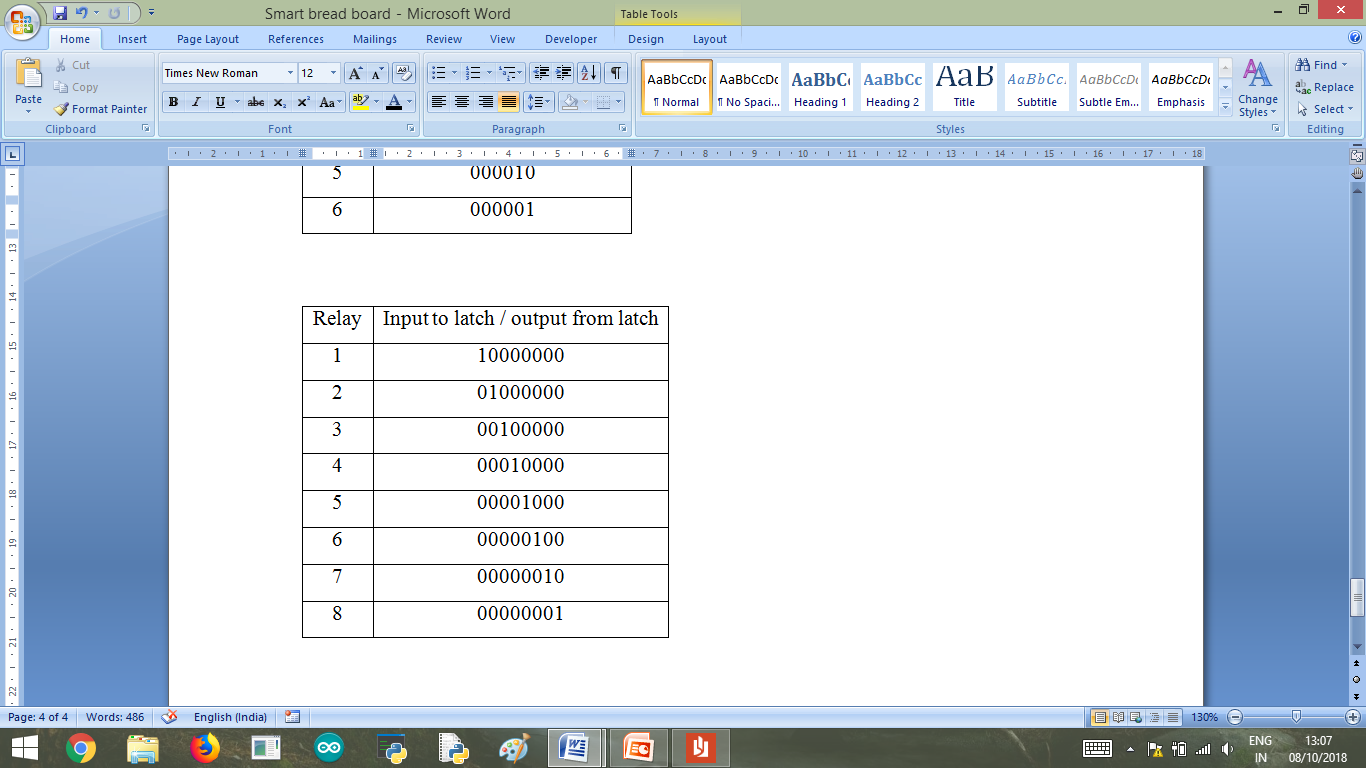


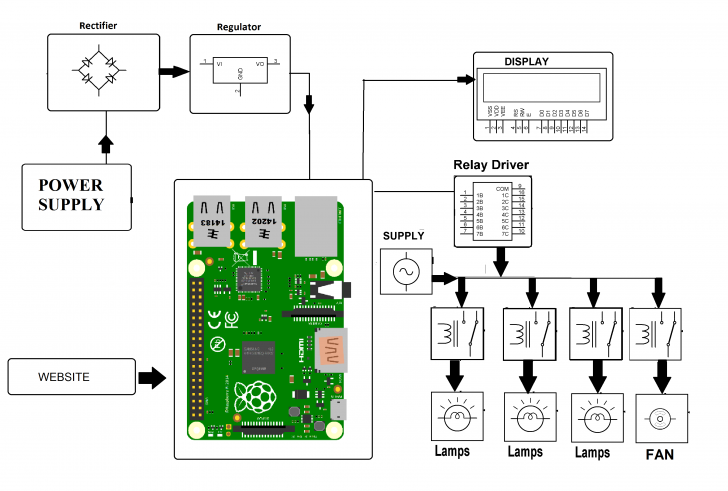
Fig 3: Interfacing relay with node lines and pins



**CONTROL SIGNALS**

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**INTERFACING RELAY WITH RASPBERRY PI 3**

**RELAY DRIVER IC(Drives high power loads)**

